

## ABSTRACT

The invention relates to a parallel inverter system, in which each inverter includes a synchronized square wave generator, a voltage given generator, a voltage regulating unit and a power amplifier unit. All of the above components are connected in parallel. The output current given by the voltage regulating unit no longer requires selection of "one out of many components," but outputs a linear combination result of outputs of all of the voltage regulating units after linear combination, thereby changing an unequal parallel connection to an equalized parallel connection. At the same time, the output square waves of all of synchronizing square wave generators are inputted into voltage given generators as the synchronizing square wave after AND. The output sine waves of all of voltage given generators after linear combination are used as given voltage of voltage regulating units.